



EC7BW-110 Series Application Note V14

ISOLATED DC-DC CONVERTER EC7BW-110 SERIES APPLICATION NOTE



Approved By:

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1. Introduction

The EC7BW-110 series of DC-DC converters offers 20 watts of output power @ output voltages of 5, 12, 15, ±12, ±15VDC with industry 2"x1"x0.4" package. It has a wide (4:1) input voltage range of 43 to 160VDC (110VDC nominal) and 3000VDC basic isolation.

Compliant with EN50155, EN45545, EN50121-3-2. High efficiency up to 90%, allowing case operating temperature range of -40°C to 105°C. Very low no load power consumption (3mA), an ideal solution for energy critical systems.

Fully protected against input UVLO (under voltage lock out), output over-current, output over-voltage and continuous short circuit conditions.

The standard control functions include remote on/off (positive or negative) and +10%, -10% adjustable output voltage (single output only).

EC7BW-110 series is designed primarily for common railway applications of 72V, 96V, 110V nominal voltage and also suitable for distributed power architectures, telecommunications, battery operated equipment and industrial applications.

2. Pin Function Description



Single Output

No	Label	Function	Description	Reference
1	+Vin	+V Input	Positive Supply Input	Section 7.1/7.2
2	-Vin	-V Input	Negative Supply Input	Section 7.1/7.2
3	+Vout	+V Output	Positive Power Output	Section 7.3/7.4
4	Trim	Trim	External Output Voltage Adjustment	Section 6.5
5	-Vout	-V Output	Negative Power Output	Section 7.3/7.4
6	Remote	Remote On/Off	External Remote On/Off Control	Section 6.4

Dual Output

No	Label	Function	Description	Reference
1	+Vin	+V Input	Positive Supply Input	Section 7.1/7.2
2	-Vin	-V Input	Negative Supply Input	Section 7.1/7.2
3	+Vout	+V Output	Positive Power Output	Section 7.3/7.4
4	-Vout	-V Output	Negative Power Output	Section 7.3/7.4
5	Com	Common	Common Power Output	Section 7.3/7.4
6	Remote	Remote On/Off	External Remote On/Off Control	Section 6.4

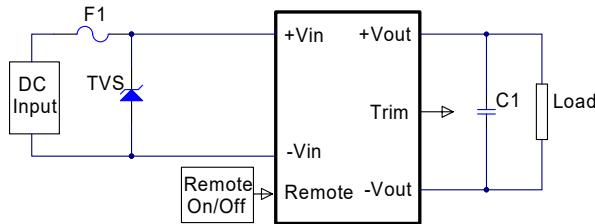


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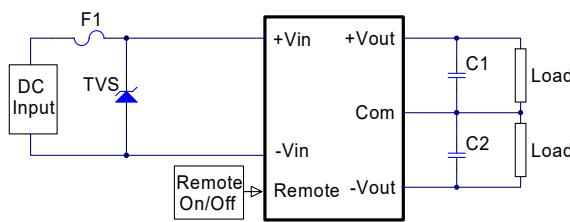
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3. Connection for Standard Use

The connection for standard use is shown below. External output capacitors (C1, C2) are recommended to reduce output ripple and noise, 1uF ceramic capacitor for all models.



EC7BW-110 single output module



EC7BW-110 dual output module

Symbol	Component	Reference
F1, TVS	Input fuse, TVS	Section 9.1
C1, C2	External capacitor on the output side	Section 7.3
Remote On/Off	External remote on/off control	Section 6.4
Trim	External output voltage adjustment	Section 6.5

4. Test Set-Up

The basic test set-up to measure parameters such as efficiency and load regulation is shown below. When testing the modules under any transient conditions please ensure that the transient response of the source is sufficient to power the equipment under test. We can calculate:

- Efficiency
- Load regulation and line regulation

The value of efficiency is defined as:

$$\eta = \frac{V_o \times I_o}{V_{in} \times I_{in}} \times 100\%$$

Where:

V_o is output voltage,
 I_o is output current,

V_{in} is input voltage,
 I_{in} is input current

The value of load regulation is defined as:

$$Load\ reg. = \frac{V_{FL} - V_{NL}}{V_{NL}} \times 100\%$$

Where:

V_{FL} is the output voltage at full load
 V_{NL} is the output voltage at no load

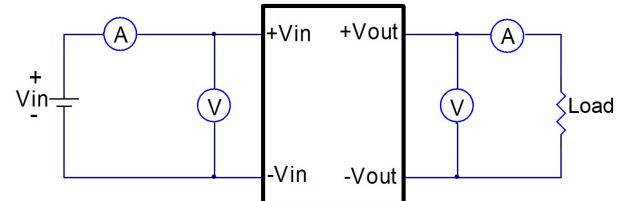
The value of line regulation is defined as:

$$Line\ reg. = \frac{V_{HL} - V_{LL}}{V_{LL}} \times 100\%$$

Where:

V_{HL} is the output voltage of maximum input voltage at full load

V_{LL} is the output voltage of minimum input voltage at full load



EC7BW-110 Series Test Setup

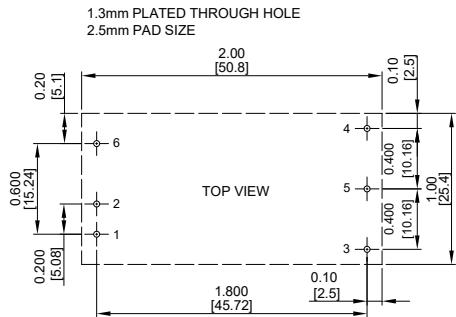
5. Recommend Layout, PCB Footprint and Soldering Information

The system designer or end user must ensure that metal and other components in the vicinity of the converter meet the spacing requirements for which the system is approved. Low resistance and inductance PCB layout traces are the norm and should be used where possible. Due consideration must also be given to proper low impedance tracks between power module, input and output grounds. The recommended footprints and soldering profiles are shown below.



EC7BW-110 Series

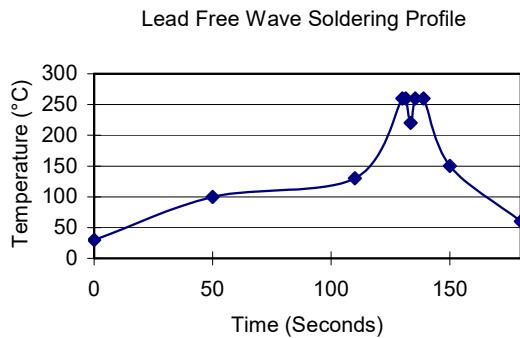
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Note: Dimensions are in inches (millimeters)

Clean the soldered side of the module with a brush, prevent liquid from getting into the module. Do not clean by soaking the module into liquid. Do not allow solvent to come in contact with product labels or resin case as this may change the color of the resin case or cause deletion of the letters printed on the product label. After cleaning, dry the modules well.

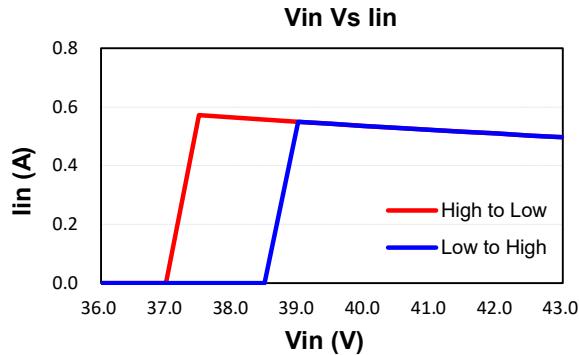
The suggested soldering iron is $420 \pm 10^\circ\text{C}$ for up to 4-10 seconds (less than 90W) used in double PCB and multilayer PCB. The other one is used in the single PCB is $385 \pm 10^\circ\text{C}$ for up to 2-6 seconds (less than 90W). Furthermore the recommended soldering profile is shown below.



6. Features and Functions

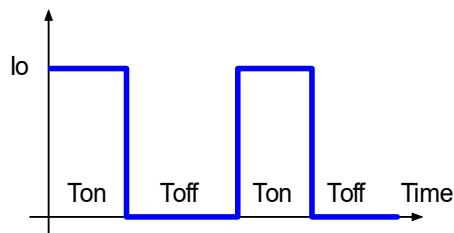
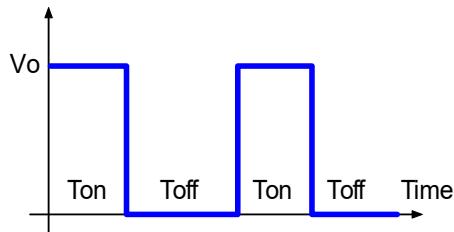
6.1 UVLO (Under Voltage Lock Out)

Input under voltage lockout is standard on the EC7BW-110 series unit. The unit will shut down when the input voltage drops below a lower threshold, and the unit will operate when the input voltage goes above the upper threshold.



6.2 Over Current/Short Circuit Protection

All models have internal over current and continuous short circuit protection. The unit operates normally once the fault condition is removed. At the point of current limit inception, the converter will go into hiccup mode protection.



6.3 Output Over Voltage Protection

The over-voltage protection consists of a zener diode to limit the out voltage.



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6.4 Remote On/Off

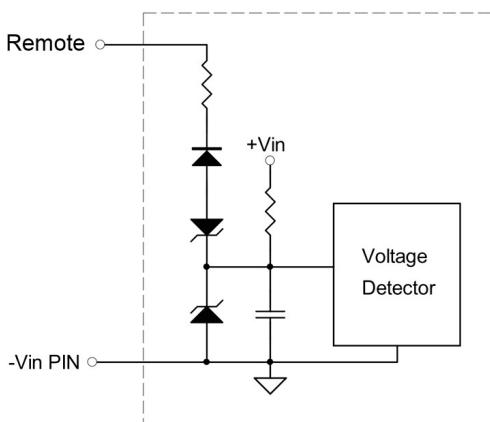
The EC7BW-110 series allows the user to switch the module on and off electronically with the remote **on/off** feature. All models are available in “positive logic” and “negative logic” (optional) versions. The converter turns on if the remote **on/off** pin is high (>3.5Vdc to 75Vdc or open circuit). Setting the pin low (0 to <1.2Vdc) will turn the converter off. The signal level of the remote **on/off** input is defined with respect to ground.

If not using the remote **on/off** pin, leave the pin open (converter will be on). Converter will be turn on in positive mode.

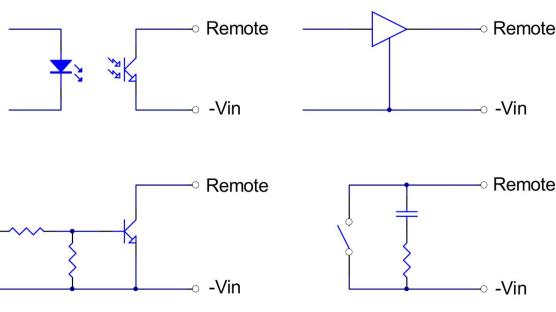
Models with part number suffix “N” are the “negative logic” remote **on/off** version. The unit turns off if the remote **on/off** pin is high (>3.5Vdc to 75Vdc or open circuit). The converter turns on if the **on/off** pin input is low (0 to <1.2Vdc). Note that the converter is off by default.

Logic State (Pin 6)	Negative Logic	Positive Logic
Logic Low	Module on	Module off
Logic High	Module off	Module on

The converter remote **on/off** circuit built-in on input side. The ground pin of input side remote on/off circuit is -Vin pin. Inside connection sees below.



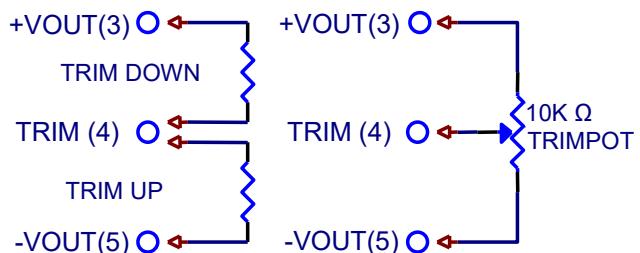
Connection examples see below.



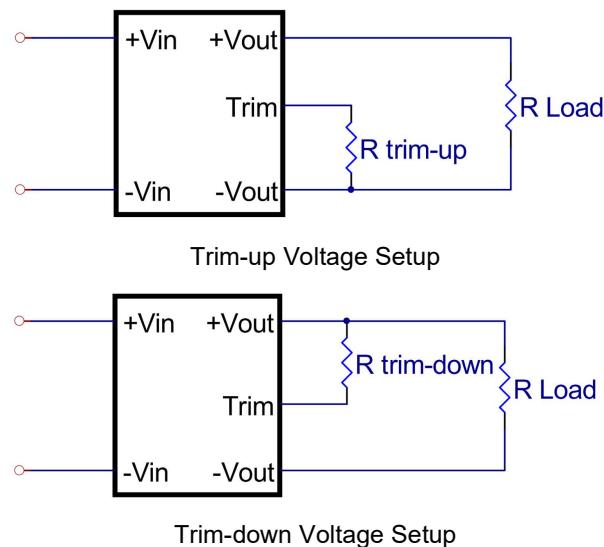
Remote On/Off Connection Examples

6.5 Output Voltage Adjustment

Output may be externally trimmed +10% to -10% (single output models only) with a fixed resistor or an external trim pot as shown (optional). Model specific formulas for calculating trim resistors are available upon request as a separate document.



In order to trim the voltage up or down, one needs to connect the trim resistor either between the trim pin and -Vout for trim-up or between trim pin and +Vout for trim-down. The output voltage trim range is +10% to -10%. This is shown:



The value of $R_{trim-up}$ defined as:

$$R_{trim-up} = \left(\frac{V_r \times R_1 \times (R_2 + R_3)}{(V_o - V_{o,nom}) \times R_2} \right) - R_t \text{ (kΩ)}$$



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Table 1 – Trim up and Trim down Resistor Values

Model Number	Output Voltage(V)	R1 (KΩ)	R2 (KΩ)	R3 (KΩ)	Rt (KΩ)	Vr (V)
EC7BW-110S05	5.0	2.32	2.32	0	8.2	2.5
EC7BW-110S12	12.0	6.8	2.4	2.32	22	2.5
EC7BW-110S15	15.0	8.06	2.4	3.9	27	2.5

Where

$R_{trim-up}$ is the external resistor in Kohm

$V_{o,nom}$ is the nominal output voltage

V_o is the desired output voltage

R1, Rt, R2, R3 and Vr are internal to the unit and are defined in Table 1

For example, to trim-up the output voltage of 5.0V module (EC7BW-110S05) by 10% to 5.5V, R trim-up is calculated as follows:

$$V_o - V_{o,nom} = 5.5 - 5.0 = 0.5V$$

$$R1 = 2.32 K\Omega$$

$$R2 = 2.32 K\Omega$$

$$R3 = 0 K\Omega$$

$$Rt = 8.2 K\Omega$$

$$Vr = 2.5 V$$

$$R_{trim-up} = \frac{2.5 \times 2.32 \times (2.32 + 0)}{0.5 \times 2.32} - 8.2 = 3.4(K\Omega)$$

The typical value of R_{trim_up}

Trim up %	5V	12V	15V
	R_{trim_up} (KΩ)		
1%	107.80	256.61	325.63
2%	49.80	117.31	149.31
3%	30.47	70.87	90.54
4%	20.80	47.65	61.16
5%	15.00	33.72	43.53
6%	11.13	24.44	31.77
7%	8.37	17.80	23.38
8%	6.30	12.83	17.08
9%	4.69	8.96	12.18
10%	3.40	5.86	8.26

The value of $R_{trim-down}$ defined as:

$$R_{trim-down} = R1 \times \left(\frac{Vr \times R1}{(V_{o,nom} - V_o) \times R2} - 1 \right) - Rt (K\Omega)$$

Where

$R_{trim-down}$ is the external resistor in Kohm.

$V_{o,nom}$ is the nominal output voltage.

V_o is the desired output voltage.

R1, Rt, R2, R3 and Vr are internal to the unit and are defined in Table 1

For example, to trim-down the output voltage of 5.0V module (EC7BW-110S05) by 10% to 4.5V, R trim-down is calculated as follows:

$$V_{o,nom} - V_o = 5.0 - 4.5 = 0.5V$$

$$R1 = 2.32 K\Omega$$

$$R2 = 2.32 K\Omega$$

$$R3 = 0 K\Omega$$

$$Rt = 8.2 K\Omega$$

$$Vr = 2.5 V$$

$$R_{trim-down} = 2.32 \times \left(\frac{(2.5 \times 2.32)}{0.5 \times 2.32} - 1 \right) - 8.2 = 1.08 (K\Omega)$$

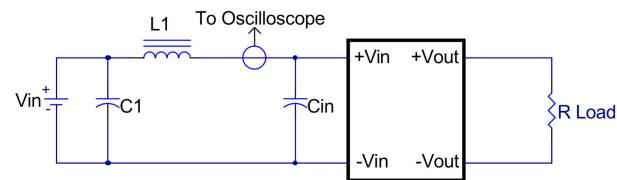
The typical value of R_{trim_down}

Trim down %	5V	12V	15V
	R_{trim_down} (KΩ)		
1%	105.48	372.59	416.08
2%	47.48	171.89	190.51
3%	28.15	105.00	115.32
4%	18.48	71.55	77.72
5%	12.68	51.48	55.17
6%	8.81	38.10	40.13
7%	6.05	28.54	29.39
8%	3.98	21.37	21.33
9%	2.37	15.80	15.07
10%	1.08	11.34	10.05

7. Input / Output Considerations

7.1 Input Capacitance at the Power Module

The converters must be connected to low AC source impedance. To avoid problems with loop stability source inductance should be low. Also, the input capacitors (C_{in}) should be placed close to the converter input pins to de-couple distribution inductance. However, the external input capacitors are chosen for suitable ripple handling capability. Low ESR capacitors are good choice. Circuit as shown as below represents typical measurement methods for reflected ripple current. C_1 and L_1 simulate a typical DC source impedance. The input reflected-ripple current is measured by current probe to oscilloscope with a simulated source inductance (L_1).



L1: 12uH

C1: None

Cin: 22uF ESR<0.2ohm @100KHz

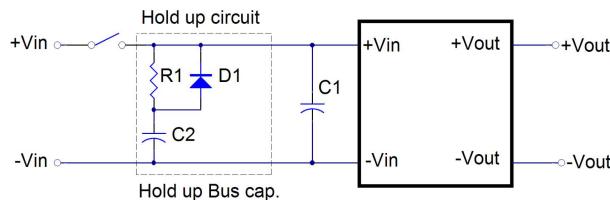


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7.2 Hold Up Time

Hold up time is defined as the duration of time that the DC/DC converter output will remain active following a loss of input power. To meet power supply interruptions, an external circuit is required, shown below.



D1:200V/10A

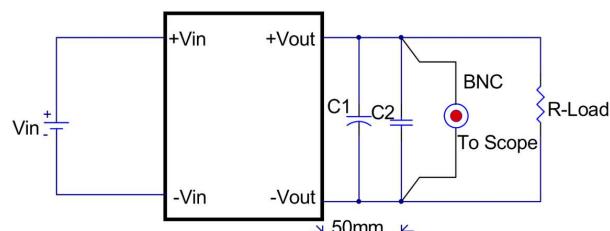
R1:100Ω/10W

C1: None

C2 (Hold up Bus cap.): See below table

Input Voltage	72Vdc	96Vdc	110Vdc
Hold up time for 10ms	180uF	82uF	68uF
Hold up time for 30ms	560uF	250uF	180uF

7.3 Output Ripple and Noise

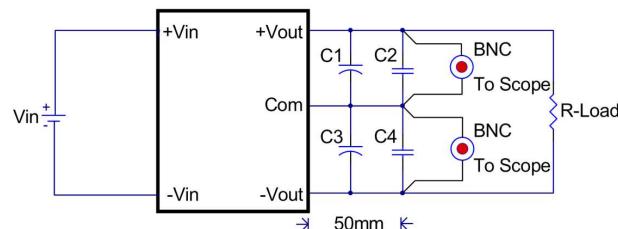


Note:

C1: None

C2: 1uF ceramic capacitor

EC7BW-110 single output module



Note:

C1 & C3: None

C2 & C4: 1uF ceramic capacitor

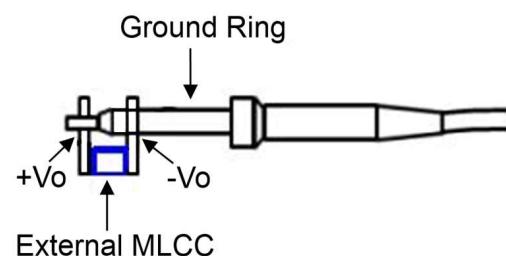
EC7BW-110 dual output module

Output ripple and noise measured with 1uF ceramic capacitor across output, A 20 MHz bandwidth oscilloscope is normally used for the measurement.

The conventional ground clip on an oscilloscope probe should never be used in this kind of measurement. This clip, when placed in a field of radiated high frequency energy, acts as an antenna or inductive pickup loop, creating an extraneous voltage that is not part of the output noise of the converter.



Another method is shown in below, in case of coaxial cable/BNC is not available. The noise pickup is eliminated by pressing scope probe ground ring directly against the -Vout terminal while the tip contacts the +Vout terminal. This makes the shortest possible connection across the output terminals.



7.4 Output Capacitance

The EC7BW-110 series converters provide unconditional stability with or without external capacitors. For good transient response, low ESR output capacitors should be located close to the point of load (<100mm). PCB design emphasizes low resistance and inductance tracks in consideration of high current applications. Output capacitors with their associated ESR values have an impact on loop stability and bandwidth. Cincon's converters are designed to work with load capacitance to see technical specifications.



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8. Thermal Design

8.1 Operating Temperature Range

The EC7BW-110 series converters can be operated within a wide case temperature range of -40°C to 105°C. Consideration must be given to the derating curves when ascertaining maximum power that can be drawn from the converter. The maximum power drawn from models is influenced by usual factors, such as:

- Input voltage range
- Output load current
- Forced air or natural convection

8.2 Convection Requirements for Cooling

To predict the approximate cooling needed for the 2"×1" module, refer to the power derating curves in **section 8.4**. These derating curves are approximations of the ambient temperatures and airflows required to keep the power module temperature below its maximum rating. Once the module is assembled in the actual system, the module's temperature should be monitored to ensure it does not exceed 105°C as measured at the center of the top of the case (thus verifying proper cooling).

8.3 Thermal Considerations

The power module operates in a variety of thermal environments; however, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding environment. The example is presented in **section 8.4**. The power output of the module should not be allowed to exceed rated power ($V_{o_set} \times I_{o_max.}$).

8.4 Power Derating

The operating ambient temperature range of EC7BW-110 series is -40°C to 85°C (derating above 73°C). When operating the EC7BW-110 series, proper derating or cooling is needed. The maximum case temperature under any operating condition should not exceed 105°C (refer to datasheet).



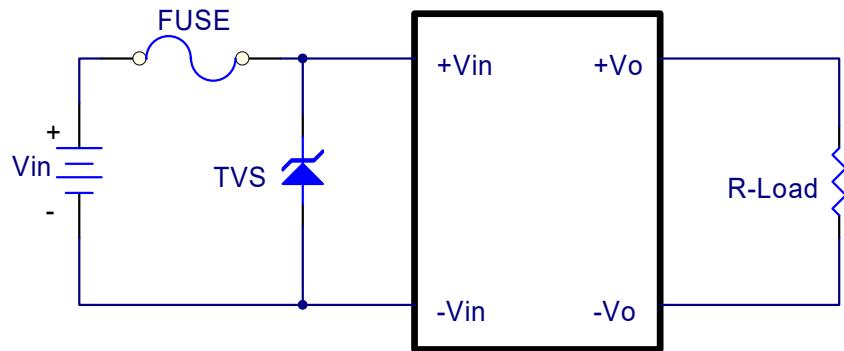
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9. Safety & EMC

9.1 Input Fusing and Safety Considerations

The EC7BW-110 series converters have no internal fuse. In order to achieve maximum safety and system protection, always use an input line fuse. We recommended a 1A time delay fuse for all models. It is recommended that the circuit have a transient voltage suppressor diode (TVS) across the input terminal to protect the unit against surge or spike voltage and input reverse voltage (as shown).

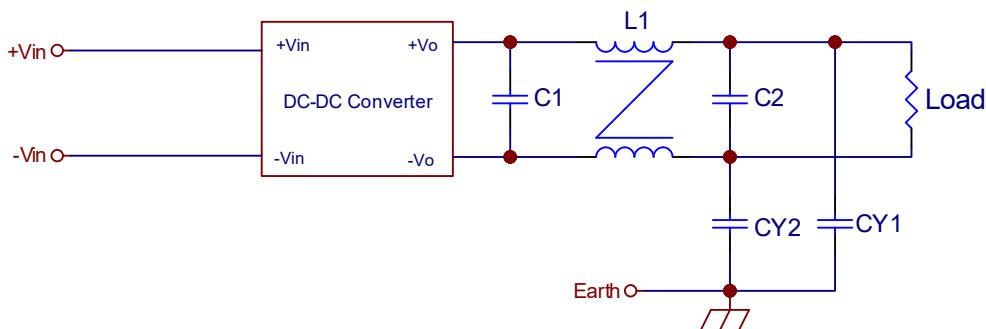


The external TVS is required if EC7BW-110 series has to meet EN61000-4-4 & EN61000-4-5.
For testing EN 50121-3-2 (railway), a TVS (P6KE180A Littelfuse) is recommended.

9.2 EMC Considerations

EMI Test Standard: EN 50121-3-2

Test Condition: Input Voltage: 110Vdc, Output Load: Full Load



Model Number	C1, C2	CY1, CY2	L1
All	0.1uF/1KV MLCC	0.047uF*2 /1KV MLCC	270uH

Note:

L1: Core: 40T0501-00H, Winding: 0.6mm*2 / 7T



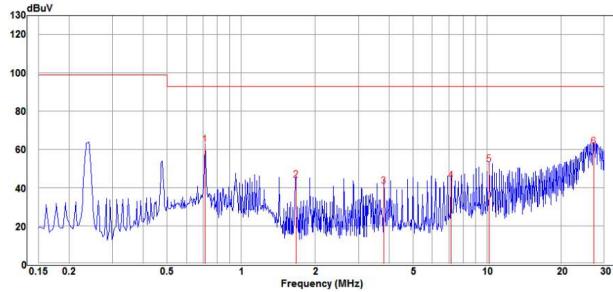
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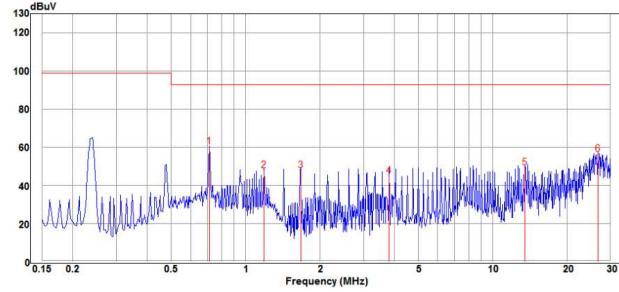
Input Conducted Emission (EN 50121-3-2):

EC7BW-110S05

Line

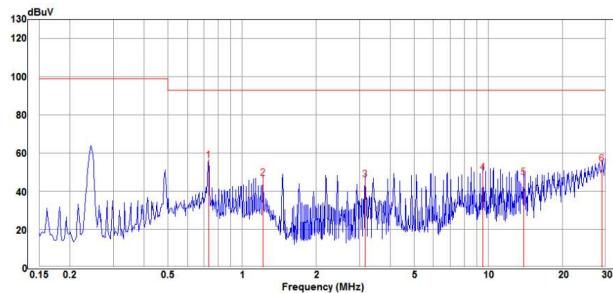


Neutral

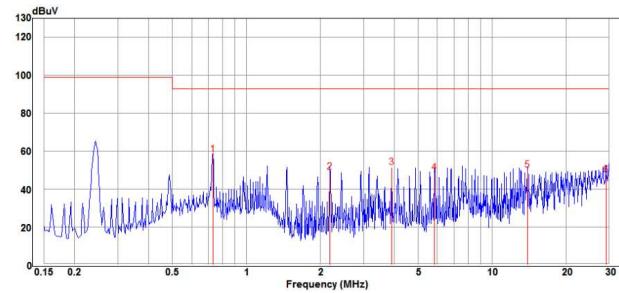


EC7BW-110S12

Line

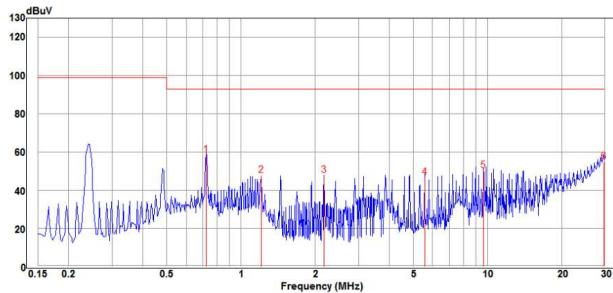


Neutral

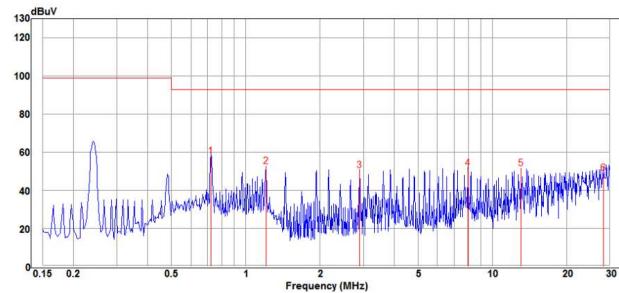


EC7BW-110S15

Line

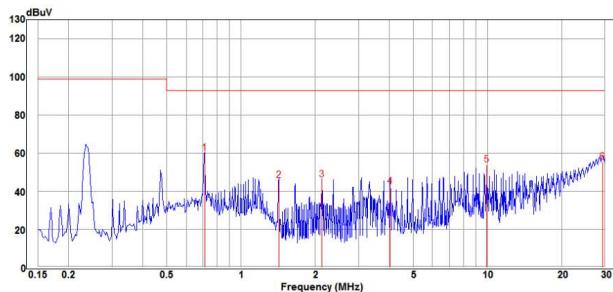


Neutral

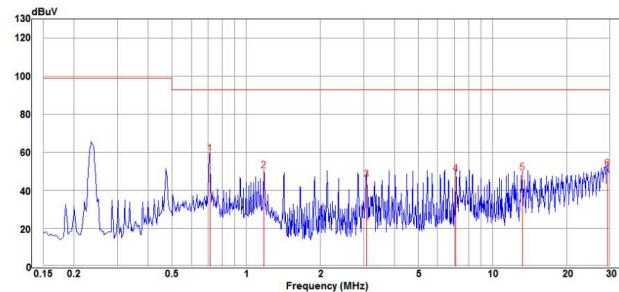


EC7BW-110D12

Line



Neutral



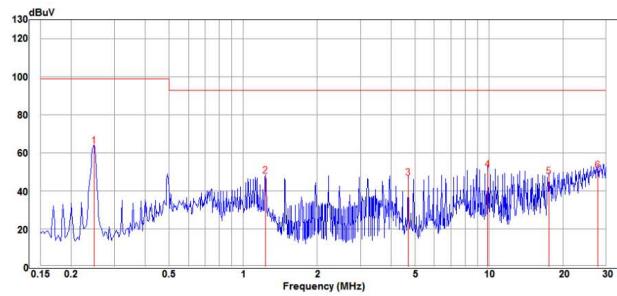


EC7BW-110 Series

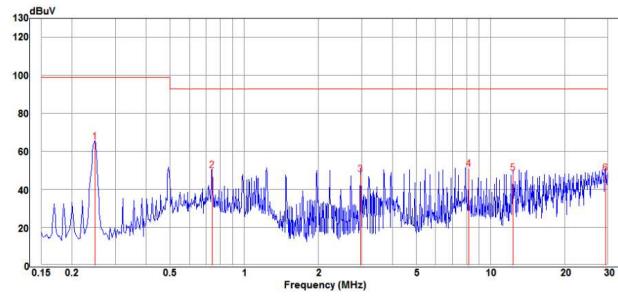
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EC7BW-110D15

Line



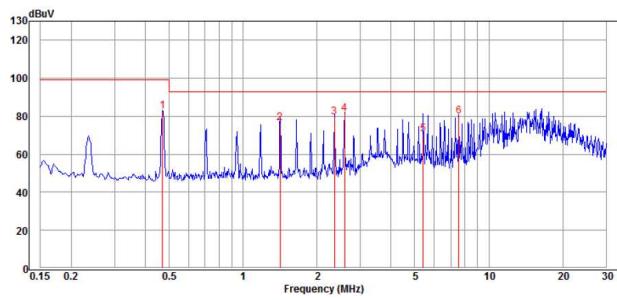
Neutral



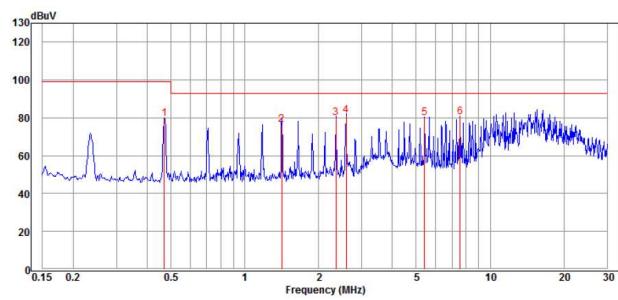
Output Conducted Emission (EN 50121-3-2):

EC7BW-110S05

Positive

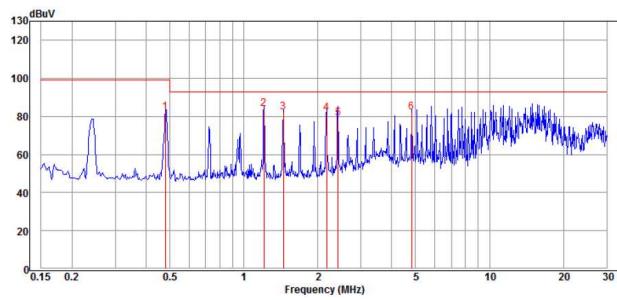


Negative

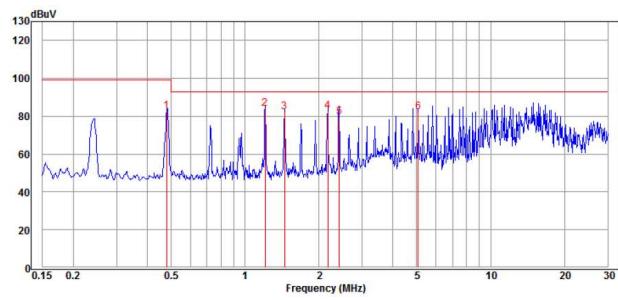


EC7BW-110S12

Positive

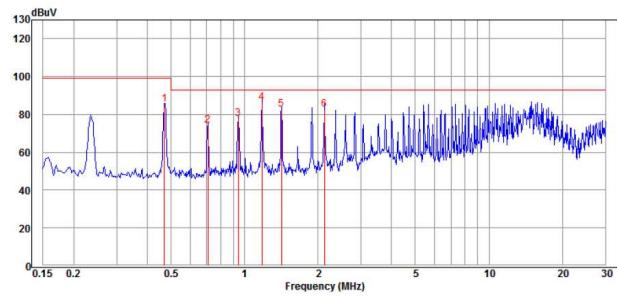


Negative

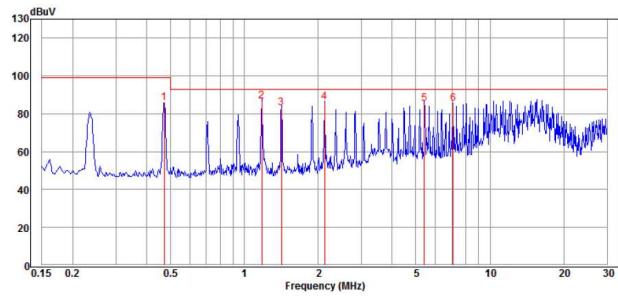


EC7BW-110S15

Positive



Negative

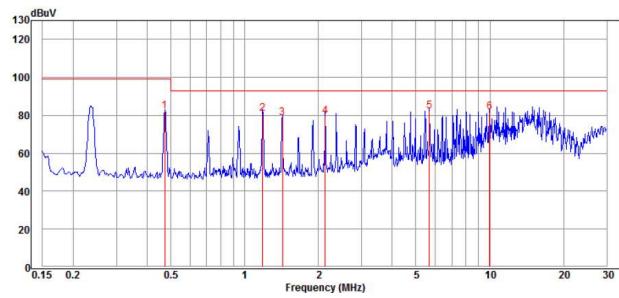




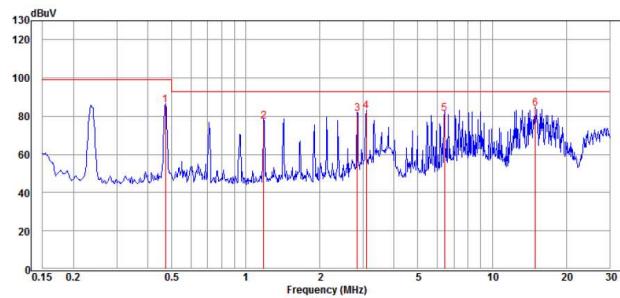
EC7BW-110 Series Application Note V14

EC7BW-110D12

Positive

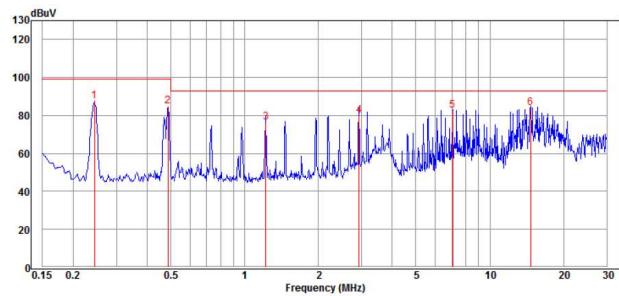


Negative

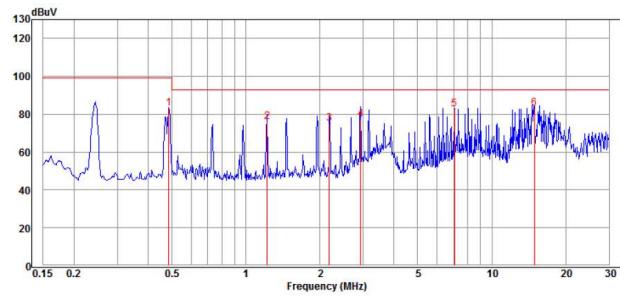


EC7BW-110D15

Positive



Negative





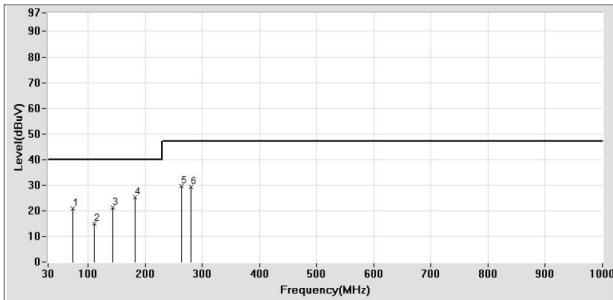
EC7BW-110 Series

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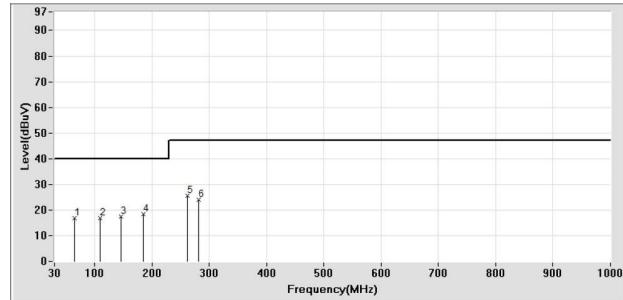
Radiated Emission (EN 50121-3-2):

EC7BW-110S05

Horizontal

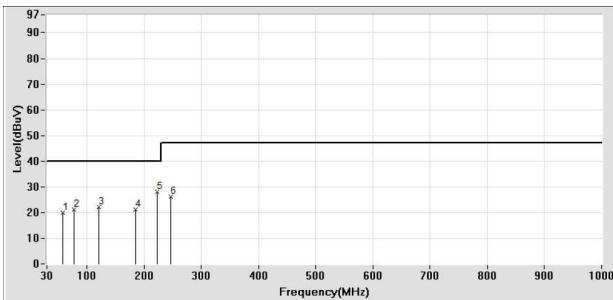


Vertical

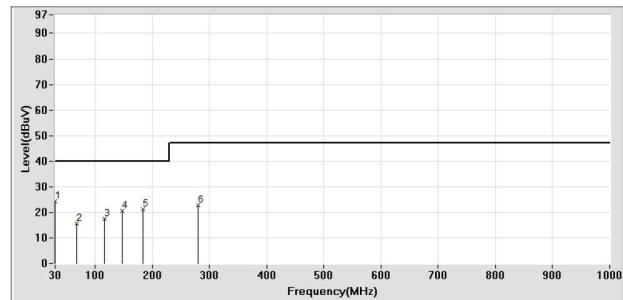


EC7BW-110S12

Horizontal

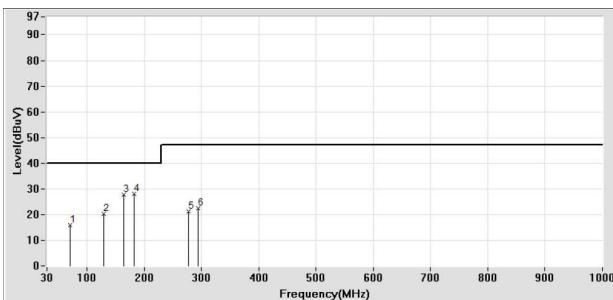


Vertical

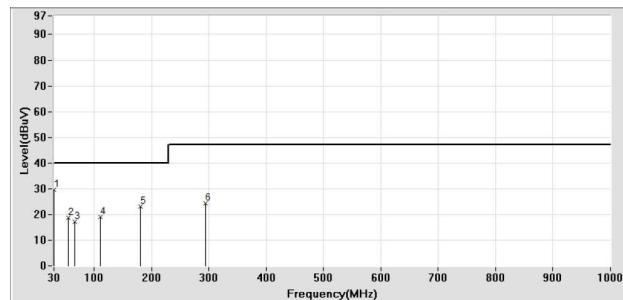


EC7BW-110S15

Horizontal



Vertical

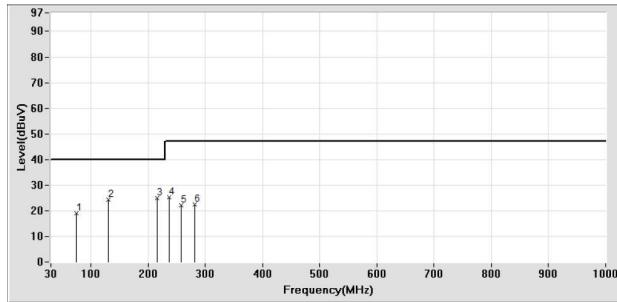




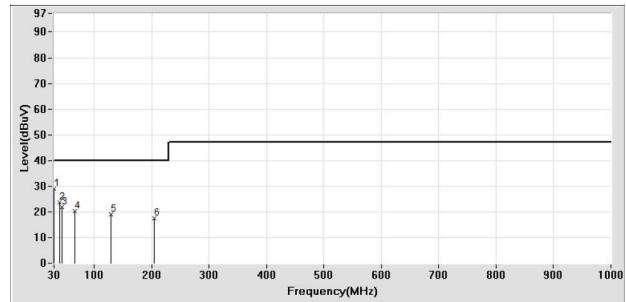
EC7BW-110 Series Application Note V14

EC7BW-110D12

Horizontal

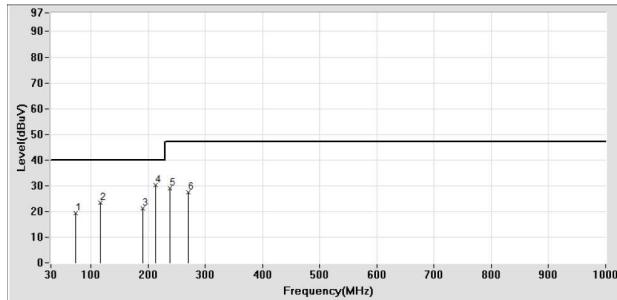


Vertical

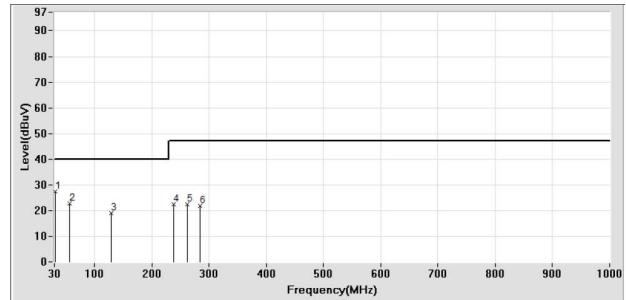


EC7BW-110D15

Horizontal



Vertical

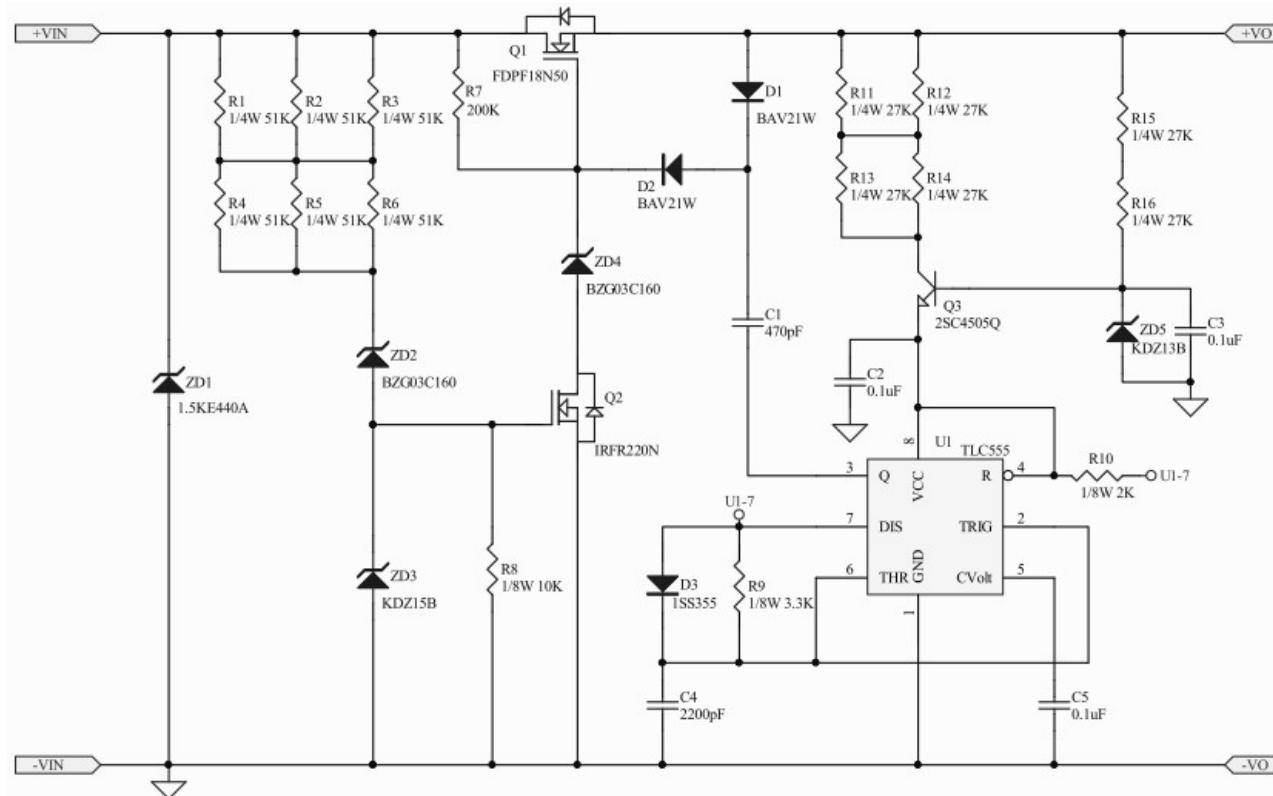




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9.3 Suggested Configuration for RIA12 Surge Test



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